

## Amendments to the Specification

Please replace the last paragraph, at the bottom of page 5, beginning at line 31, and continuing to page 6, line 8 with the following paragraph:

~~A selection~~Selection logic 105 is illustrated within packet buffer system 101 and is adapted to manage how processed packets are selected to be sent out of queue to egress of the processing system after processing is complete. ~~Dequeue logic~~selection logic 105 is illustrated as connected by control path to a common access line shared by all of the queues in set 104a-n as was described above with reference to direction logic 103. A processing core 106 is illustrated in this embodiment and is adapted to process data packet information while data packets are in the system. ~~Core~~Processing core 106 is logically connected through selection logic 105 to queue set 104a-n by a control line.

Please replace the paragraph beginning on line 12 of page 13 with the following paragraph:

~~A packet generator~~Speculative Packet Construction 309 is provided within predictor 202 and adapted to construct the speculative packet or packet information according to the results received for each separable component. In one embodiment of the invention, certain components and their predictor blocks may be disabled while other ones of these are enabled. Freedom of configuration is assumed in this example. Block 305 accesses updated history on each selected component and performs a prediction based on that history, the predicted value incorporated in the packet construction.